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BOX AF

Response # 14 6-9-03 J. Artis

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450:

By: _____ Date: May 27, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas Peter Haneder et al.

Applic. No. : 09/801,209

Filed : March 7, 2001

Title : Ferroelectric Transistor, Use Thereof In A Memory Cell Configuration And Method Of Producing The Ferroelectric Transistor

Please ENTER T.L 06/14/03

Examiner : Thao X. Le

Group Art Unit : 2814

R E S P O N S E under 37 C.F.R. § 1.116

BOX AF
Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

Responsive to the final Office action dated February 25, 2003

kindly consider the following remarks: